

Fig. 1
PRIOR ART

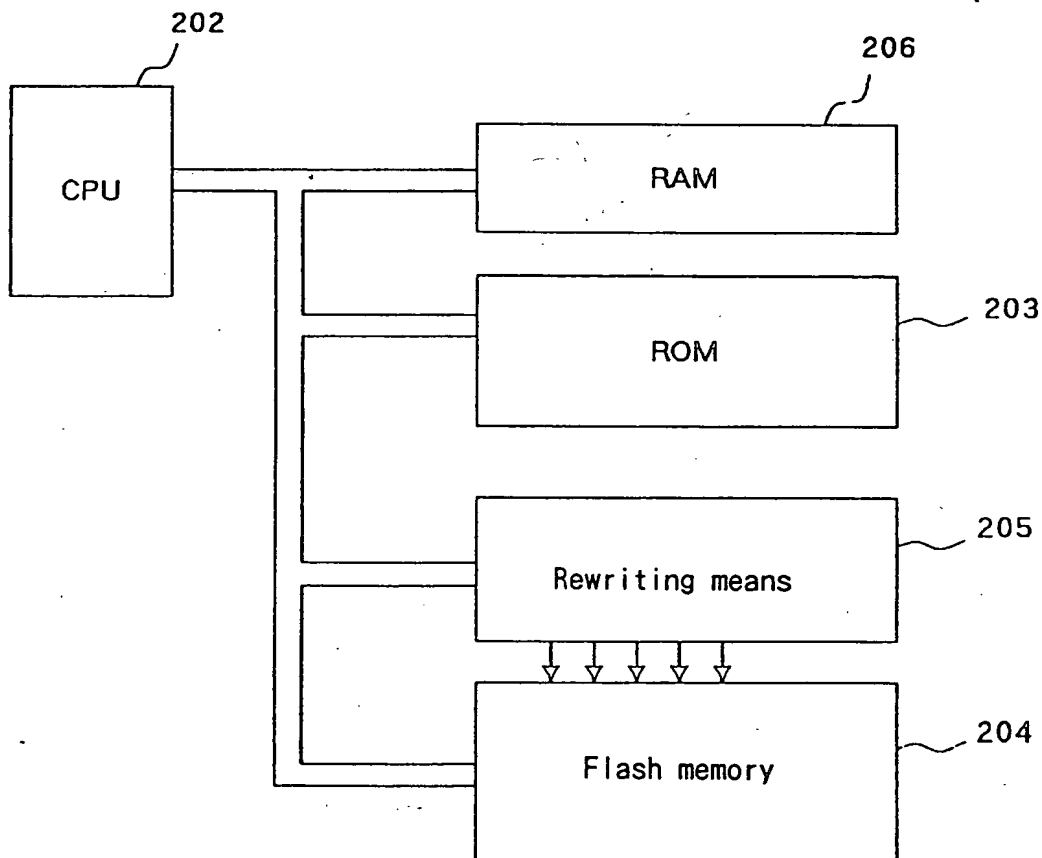




Fig. 2
PRIOR ART

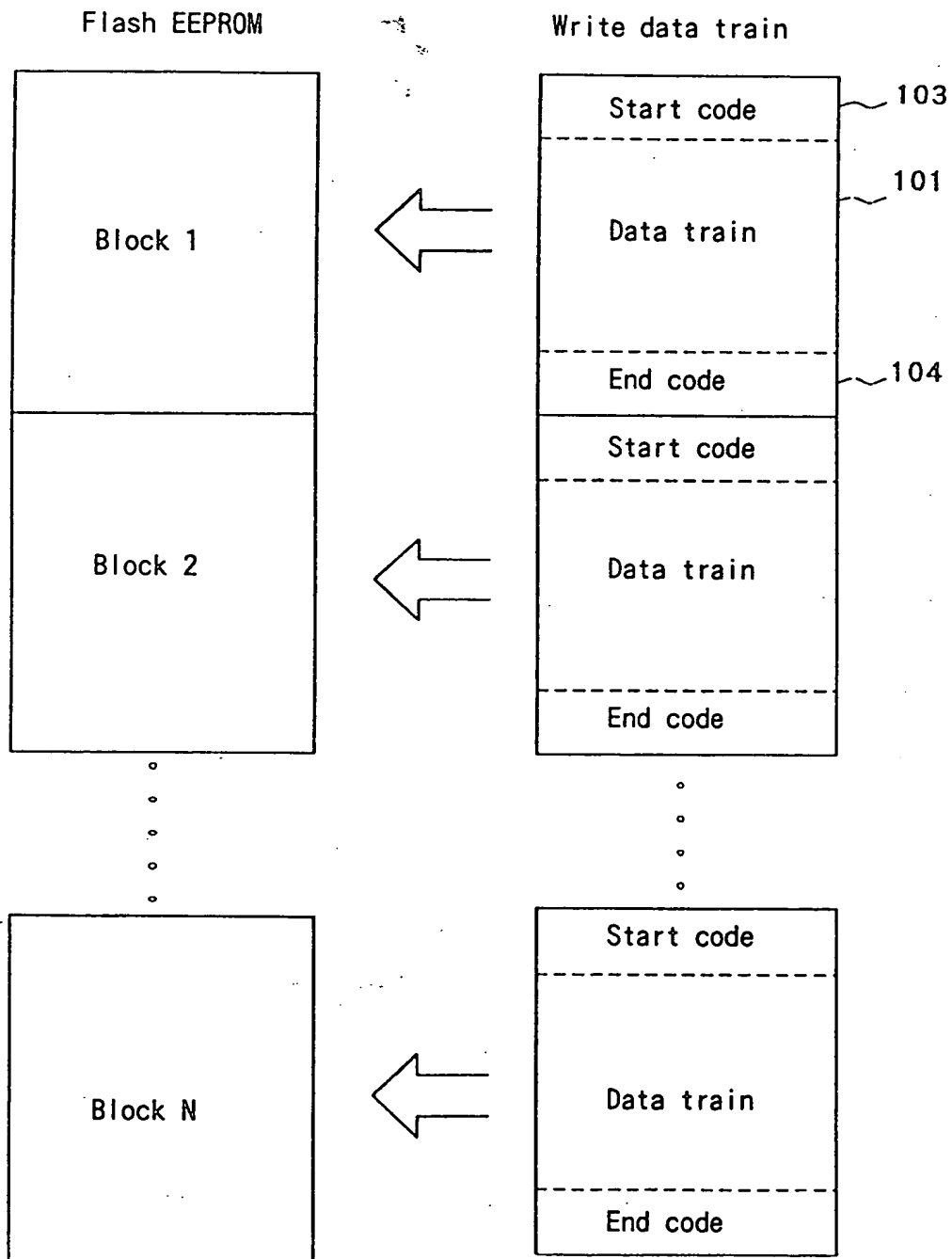


Fig. 3
PRIOR ART

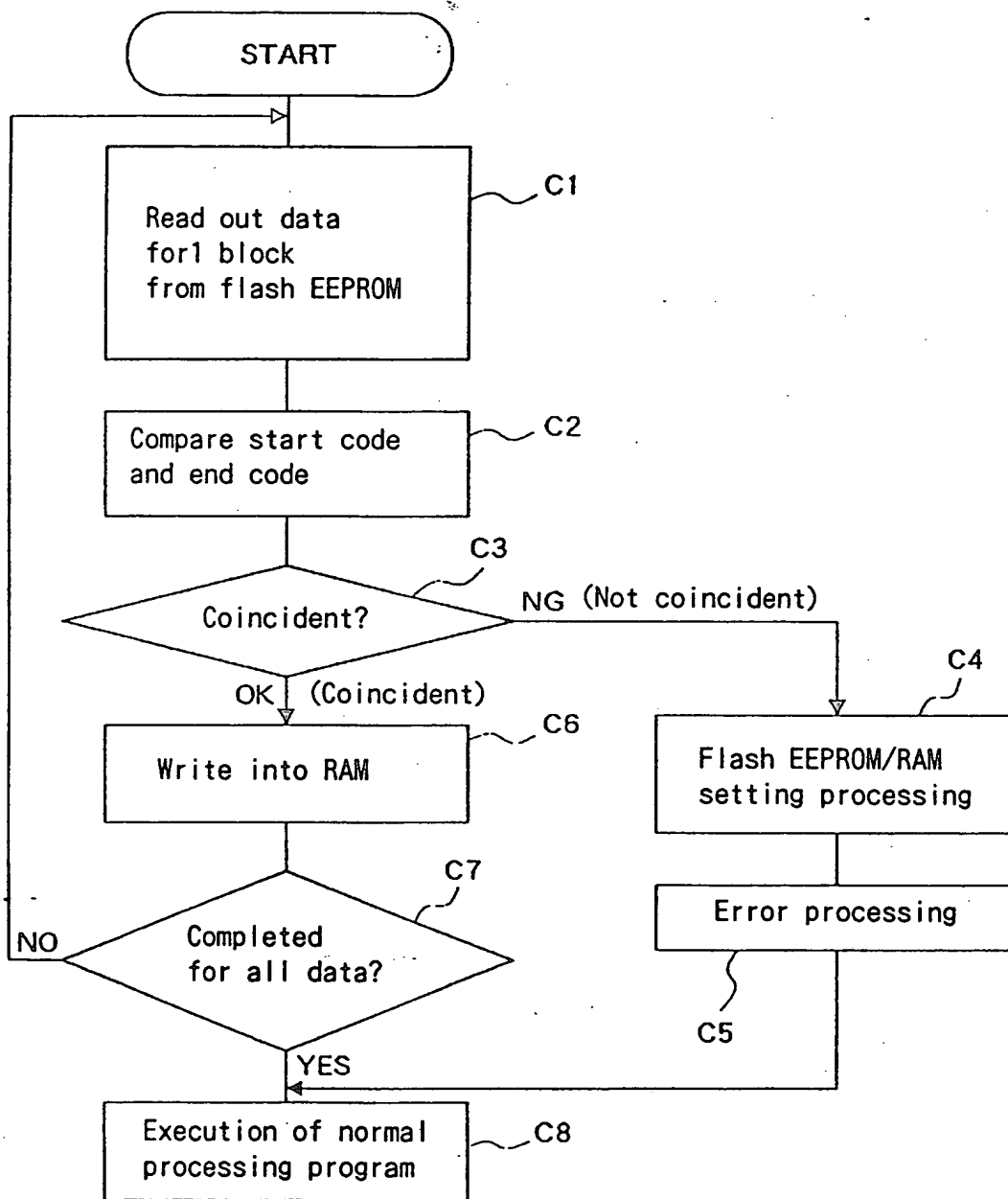


Fig. 4

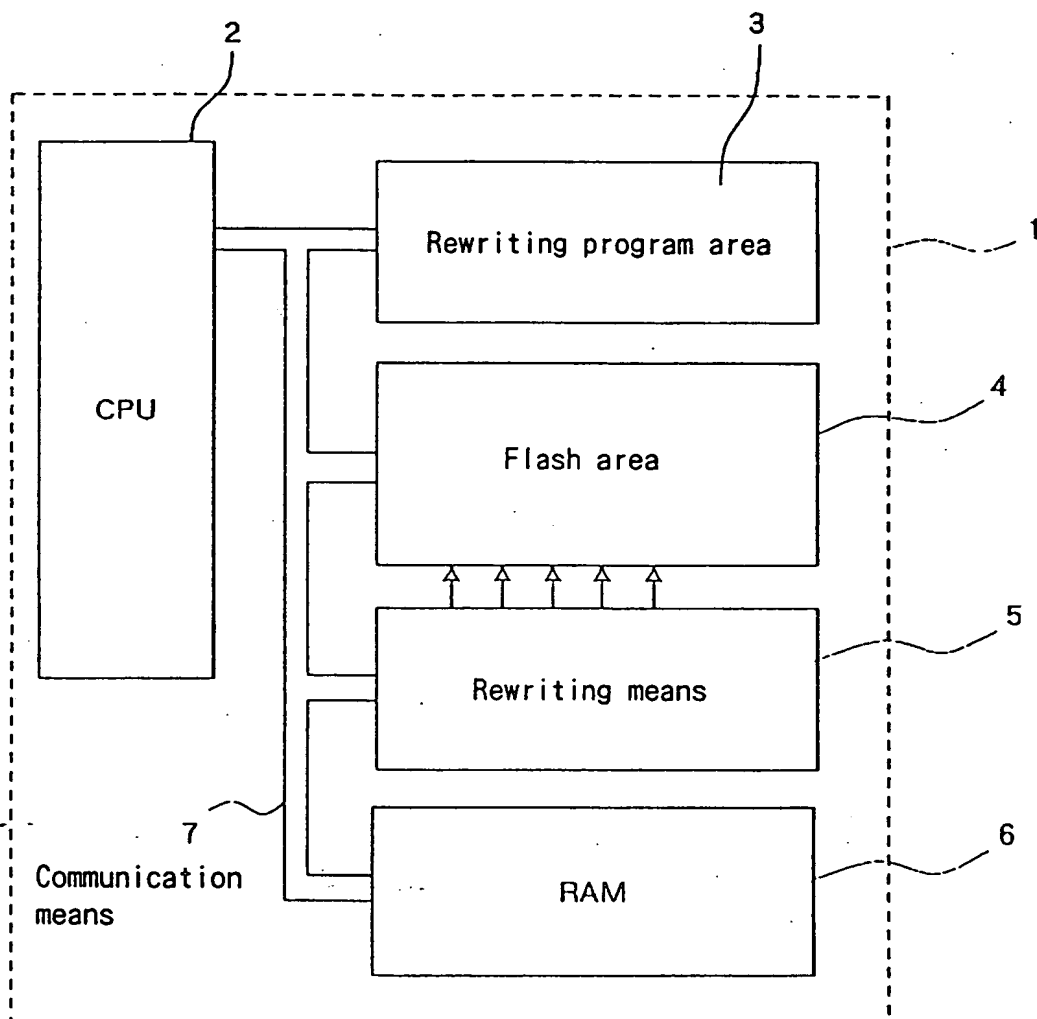
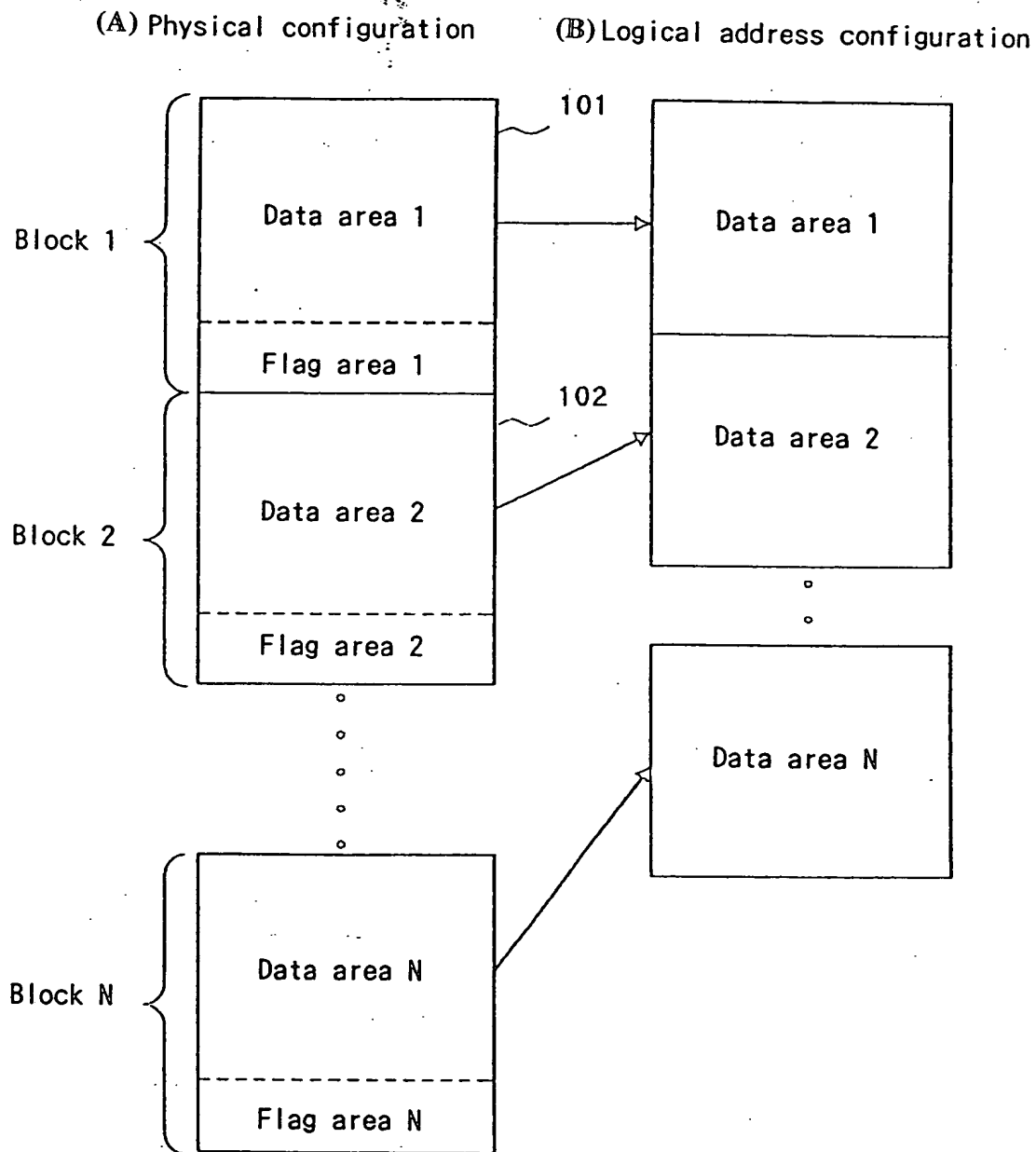


Fig. 5



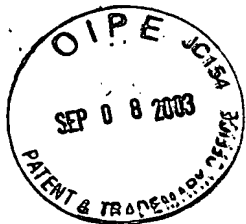


Fig. 6

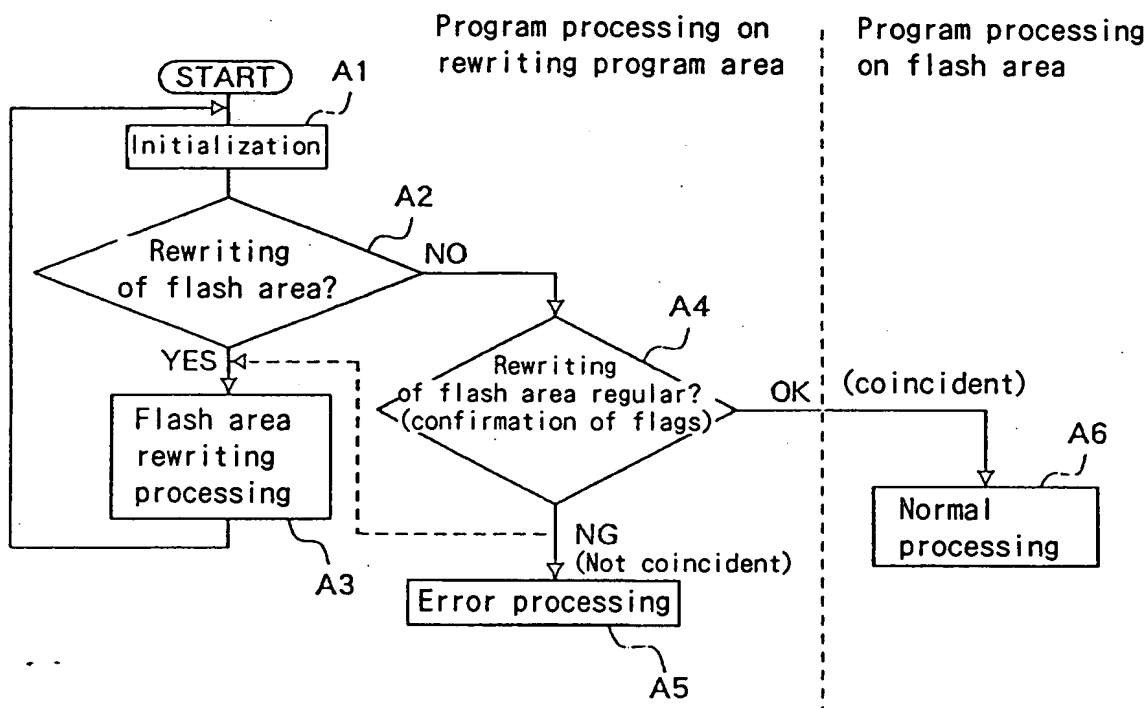


Fig. 7

(A) Physical configuration

(B) Example of address map

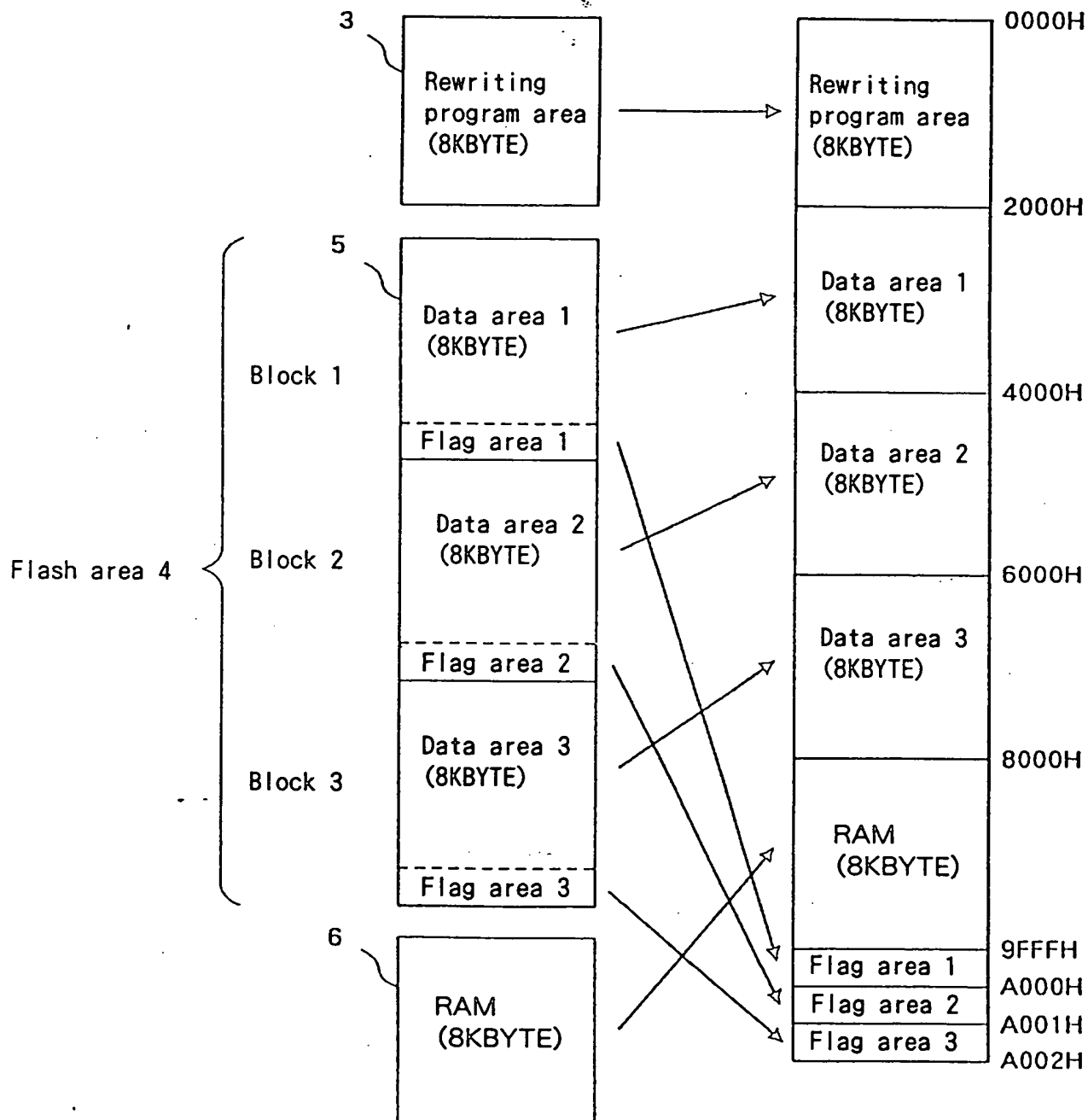




Fig. 8

BIT7	BIT6	BIT5	BIT4	BIT3	BIT2	BIT1	BIT0
Verify error flag	Blank error flag	Rewriting end flag	Verify end flag	Write end flag	Black check end flag	Eraseure end flag	Rewriting start flag

Fig. 9

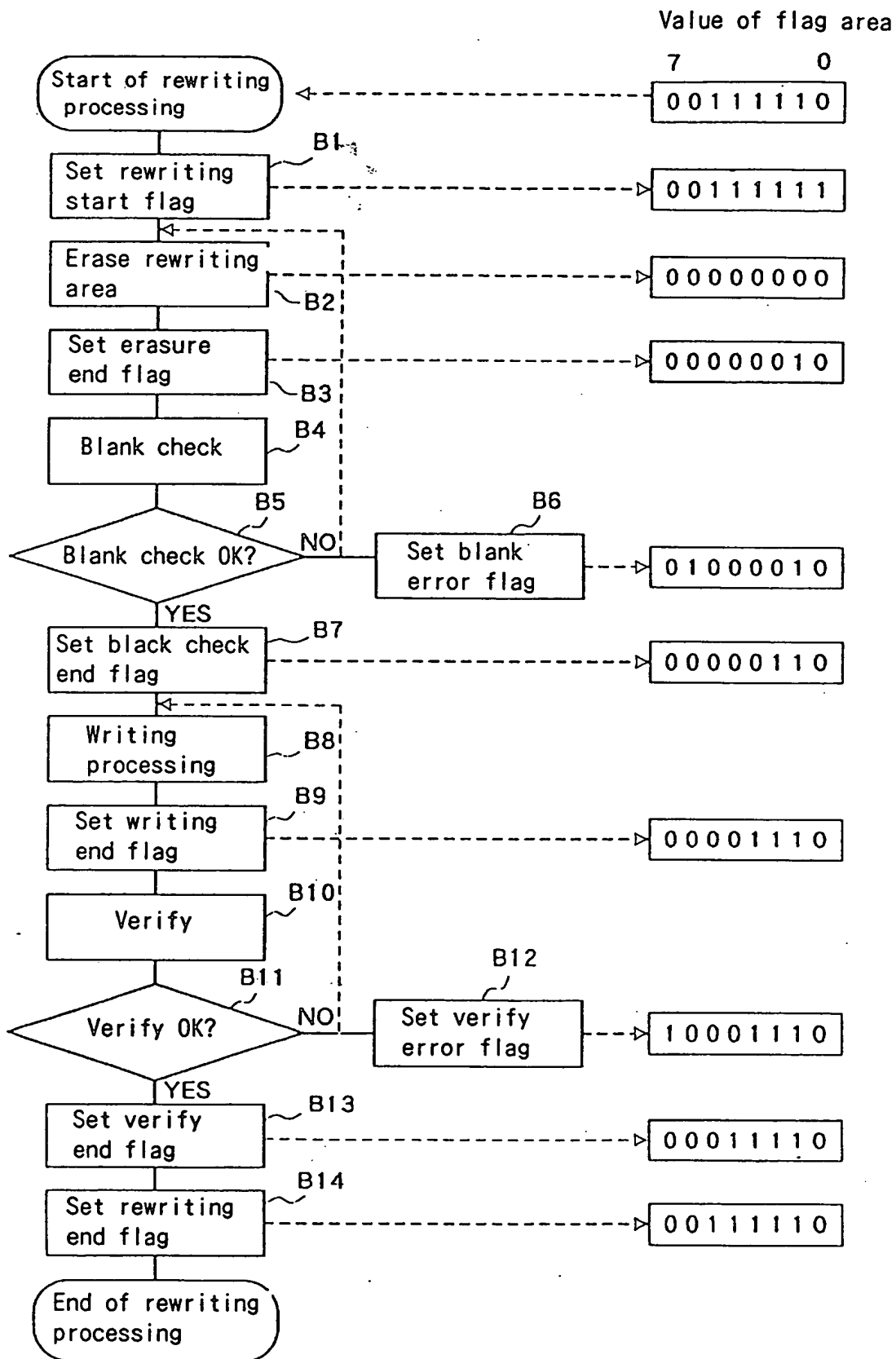


Fig. 10

